

PATENT
8070-1005

IN THE U.S. PATENT AND TRADEMARK OFFICE

In re application of: Akiro YOSHINO
Appl. No.: Conf.:
NEW NON-PROVISIONAL
Filed: Group:
November 20, 2003
Title: Examiner:
NONVOLATILE SEMICONDUCTOR MEMORY DEVICE
AND METHOD FOR MANUFACTURING SAME

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

November 20, 2003

Sir:

The following preliminary amendments and remarks are respectfully submitted in connection with the above-identified application.

Amendments to the Specification begin on page 2 of this paper.

Remarks begin on page 3 of this paper.

An **Appendix** is attached following the signature page of this paper.